

Keysight Technologies Probing Solutions for Logic Analyzers

Data Sheet



Bring the full power of your Keysight Technologies, Inc. logic analyzer to your project with high quality probing solutions

- Wide range of solutions to meet your measurement needs
- Soft Touch Connectorless probing
- High-density, high-performance probing solutions
- General-purpose probing



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Reliable Connections Ensure Accuracy

Signal frequency content drives probing solutions

Faster clock rates demand tighter timing tolerances, such as setup and hold specifications. Systems with faster clock rates usually have shorter rise and fall times. Signals with shorter transition times have more high frequency content and are more susceptible to high frequency analog problems such as cross talk, reflections, ground bounce, noise and emissions. Susceptibility of a system to analog problems relates to the transition times of the signals, not the clock rate. A system with slow transition times cannot have high clock rates. However, it is possible for a system with slower clock rates to have signals with very fast transition times.

General-purpose probing solutions provide the analog bandwidth required to run each logic analyzer module at its maximum clock rate. The high input impedance of these probes, especially at high frequencies, presents a minimal load to most systems. Systems that are operating with little margin should be designed with consideration for both the system components and the input impedance of the probing solution being used during debug. Input impedance specifications or equivalent load diagrams can be found for each of the probing solutions described in this document.

Which Logic Analyzer?

Keysight logic analyzers have two methods of connection to the probes. One uses a 3M-style connector with two rows of 20 pins on 0.1-inch centers, as illustrated in Figure 1.1. Probes for these analyzers are identified in this document as "for analyzers with 40-pin pod connectors."

The other style uses a 90-pin, high-density connector, as illustrated in Figure 1.2. Probes for these analyzers are identified in this document as "for analyzers with 90-pin pod connectors."

Currently available Keysight logic analyzers in these two groups are as follows:



- Impedance

High input impedance ensures minimum intrusion on your circuit. Although many probes might be acceptable for lower frequencies, capacitive loading becomes significant at higher frequencies. The Keysight probing products perform over a wide frequency spectrum.

– Ruggedness

Probes with quality mechanical design provide solid electrical connections. Intermittent open circuits would only add one more variable to your debugging equation. Keysight probes are mechanically designed to relieve strain and ensure rugged, reliable connection.

- Immunity to Noise
 Electromagnetic noise can corrupt data captured by the logic analyzer.
 Keysight probing solutions are designed for a high immunity to transient noise.
- Performance

Keysight logic analyzers have frontend circuitry that supports the state and timing specifications of the analyzer. This circuitry, together with the Keysight probing solutions described in this document, will accurately capture the target signals at the specified clock rates.

Other considerations

Physical connection compatibility between various Keysight probes may allow you to mix and match a variety of probes and accessories. However, a probe accessory designed for slower clock speeds will not deliver high-speed target performance simply because it is used with a higher speed analyzer module. Also, the serial connection of multiple probe leads and/or accessories will degrade signal integrity.

Probe Selection Guide for All Keysight Logic Analyzers

Compatible with Keysight models 16910A/11A, 16800 Series, 16750/51/52A/B, 1674X Series, 1671x Series, 165xx Series modules, 1690 Series, 1680 Series, 1670 Series, 1660 Series, 1650 Series, and E9340 logic analyzers.

| | Soft touch connecto | rless probes | | | | General purpose |
|--------------------------------|---|--|--|--|--|---|
| | Supplied with five re | etention modules | | Samtec probe | Mictor probe | flying lead set |
| Model number | E5396A | E5404A | E5394A | E5385A | E5346A | E5383A |
| Application | Quick connection to r header designed into | nany channels in a sm the target system | all footprint without a | Quick connection to many signals in a small footprint | Quick connection to many signals in a small footprint | Flexible connection to individual signals |
| Number of | 17 | 34 | 34 | 34 | 34 | 17 |
| channels | 16 data, 1 clock | 32 data, 2 clock | 32 data, 2 clock | 32 data, 2 clock | 32 data, 2 clock | 16 data, 1 clock |
| Supported signal types | All probes: single-en | ded clock, single-ende | ed data | | | |
| Maximum data rate | > 2.5 Gb/s | > 2.5 Gb/s | > 2.5 Gb/s | 1.5 Gb/s | Equivalent to the logic analyzer data rate the probe is attached to | Equivalent to the logic analyzer data rate the probe is attached to |
| Minimum signal amplitude | 500 mV p-p | 500 mV p-p | 500 mV p-p | 500 mV p-p | 500 mV p-p ¹ | 600 mV p-p |
| Connection to target system | Requires half-size soft touch footprint designed into the target | Requires Pro Series soft touch footprint designed into the target | Requires original soft touch footprint designed into the target | Requires 100-pin Samtec connector designed into the target system | Requires 38-pin Mictor connector designed into the target system ² | Compatible with a wide assortment of accessories to connect to individual leads |
| Input capacitance | < 0.7 pF | < 0.7 pF | < 0.7 pF | 1.5 pF | 3.0 pF | 1.5 pF |
| Additional supplies | Additional five retention modules | Additional five retention modules | Additional five retention modules | See Table 1 page 29 for pc board connectors and shrouds | See Table 1 page 29 for pc board connectors and shrouds | See Figure 2.3 page 9 for additional leads and grabbers |
| Orderable as | Order kit E5396-68702 | Order kit E5403A | Order kit E5387-68701 | | | |

1. Model E5339A low voltage Mictor probe = 250 mV p-p

2. Model E5351A Unterminated Mictor probe requires isolation networks to be provided on the target system. See page 27 for details.

Probe Selection Guide for All Keysight Logic Analyzers (Continued)

Compatible with Keysight logic analyzers U4154A, 16962A, 16951B, 16950A/B, 16760A, 16756A, 16755A, 16754A, and 16753A.



Soft touch connectorless probes All soft touch probes are supplied with 5 retention modules

| | | E5406A | | | |
|---------------------|-----------------------------|------------------------------|---------------------------|-------------------------------------|-------------------------------------|
| Model number | E5398A | E5402A ¹ | E5390A | E5405A | E5387A |
| Application | Quick connection to many | channels in a small footprin | t without a header design | ed into the target | |
| Number of channels | 17 | 34 | 34 | 17 | 17 |
| | 16 data, 1 clock | 32 data, 2 clocks | 32 data, 2 clocks | 16 data, 1 clock | 16 data, 1 clock |
| Supported signal | Differential or single-ende | d clock single-ended data | | Differential or single-end | ed clock and or data |
| types | | | | | |
| Maximum data rate | > 2.5 Gb/s | > 2.5 Gb/s | > 2.5 Gb/s | > 2.5 Gb/s | > 2.5 Gb/s |
| Minimum signal | 250 mV _{p-p} | 250 mV _{p-p} | 250 mV _{p-p} | V _{max} - V _{min} | V _{max} - V _{min} |
| amplitude | | | | 200 mV | 200 mV |
| Connection to | Requires half-size soft | Requires Pro Series soft | Requires original | Requires Pro Series soft | Requires original |
| target system | touch footprint designed | touch footprint designed | soft touch footprint | touch footprint designed | soft touch footprint |
| | into the target | into the target | designed into the | into the target system | designed into the |
| | | | target | | target system |
| Input capacitance | < 0.7 pF | < 0.7 pF | < 0.7 pF | < 0.7 pF | < 0.7 pF |
| Kit of 5 additional | E5396-68702 | E5403A | E5387-68701 | E5403A | E5387-68701 |
| retention modules | | | | | |

1. The E5402A Soft Touch Pro probe is a low profile right angle version of the E5406A above.

Probe Selection Guide for All Keysight Logic Analyzers (Continued)

Compatible with Keysight logic analyzers U4154A, 16962A, 16951B, 16950A/B, 16760A, 16756A, 16755A, 16754A, and 16753A.







| | Samtec probes | | Mictor probes | General purpose flying lead sets | | |
|-------------------|--------------------------------|-------------------------------------|-------------------------|----------------------------------|-------------------------------------|--|
| Model number | E5378A | E5379A | E5380A/B | E5382A | E5381A | |
| Application | Quick connection to many chai | nnels in a small footprint | t | Flexible connection to | many signals | |
| Number of | 34 | 17 | 34 | 17 | 17 | |
| channels | 32 data, 2 clocks | 16 data, 1 clock | 32 data, 2 clocks | 16 data, 1 clock | 16 data, 1 clock | |
| Supported signal | Differential or | Differential or | Single-ended clock | Differential or | Differential or single- | |
| types | single-ended clock single- | single-ended clock | single-ended data | single-ended clock | ended clock and or data | |
| | ended data | and or data | | single-ended data | | |
| Maximum data | 1.5 Gb/s | 1.5 Gb/s | 600 Mb/s | 1.5 Gb/s | 1.5 Gb/s | |
| rate | | | | | | |
| Minimum signal | 250 mV _{p-p} | V _{max} - V _{min} | 300 mV _{p-p} | 250 mV _{p-p} | V _{max} - V _{min} | |
| amplitude | | 200 mV | | | 200 mV | |
| Connection to | Requires 100-pin Samtec | Requires 100-pin | Requires 38-pin Mictor | Compatible with a | Compatible with a | |
| target system | connector designed into the | Samtec connector | connector designed into | wide assortment | wide assortment of | |
| | target system | designed into the | the target system | of accessories to | accessories to connect to | |
| | | target system | | connect to individual | individual leads | |
| | | | | leads | | |
| Input capacitance | 1.5 pF | 1.5 pF | 3.0 pf | 1.3 pF | 0.9 pF | |
| Additional | See Table 8 page 60 for shroud | is and pc board connect | ors | See Table 5 | See Figure 5.4 | |
| supplies | | | | page 36 | page 39 | |

Note: E5386A half-channel transition adapter provides transition between probes and 16760A logic analyzer cables. Use to reduce the number of probes and connectors required to run in half channel mode. Adapter maps to even channels to all pins of an E5387A, E5379A, E5387A, E5390A, E5405A, or E5406A.

Selecting the Optimum Probing Strategy

What is the best way to probe your signals, given their unique characteristics?

| Available probing of | options for all Keysight logic analyzers | |
|---------------------------------|---|---|
| | Connectorless | Connector Samtec |
| Connection to the target system | Requires appropriate pro series soft touch or original soft touch footprint designed into the target system. Retention module is used for alignment and mechanical retention only. | Requires 100-pin Samtec connector designed into the target system |
| Advantages | Reduces cost and shortens the design cycle by eliminating a connector Eliminates the capacitive loading of a connector, which gives you the lowest-loading (less than 0.7 pF), highest-performance (> 2.5 Gb/s rate) logic analyzer probing option available Pliable micro spring-pin design with four-point crown tip allows you to easily attach and get a reliable, repeatable contact even for contaminated or uneven board surfaces Flow through signal routing streamlines design flow and maintains differential pair spacing to ensure constant differential-mode impedance and virtually eliminate stubs Acquire high-speed single-ended or differential signals without impacting the performance of your circuit, while providing an accurate representation to the logic analyzer Provides ability to attach retention module to probe and browse multiple signals by pressing the probe against the target device Compatible with all board finishes, including lead free | High-performance connector solution (1.5 pF loading, 1.5 Gb/s data rate) Supports single-ended and differential signals 3 times the performance and half the loading of Mictor solution |
| Disadvantages | Requires up-front design of probe footprint on PCB | Added cost to include connector Requires up-front design of connector on PCB |

Selecting the Optimum Probing Strategy (Continued)

What is the best way to probe your signals, given their unique characteristics?

| Available probing op | tions for all Keysight logic analyzers | |
|----------------------|--|--|
| | Connector Mictor | Flying leads |
| | | |
| Connection to the | Requires 38-pin Mictor connector designed into the target | Connects to individual, widely dispersed signals at IC pins, |
| target system | system | traces, pads, vias |
| Advantages | Reliable and cost-effective solution for lower data rates (600 Mb/s) | High-performance accessories are based on award winning, InfiniiMax scope probes |
| | Supports single-ended signaling | Compatible with a wide variety of accessories to connect |
| | – 3.0 pF capacitive loading | to IC pins, traces, pads, vias |
| | | Maintains a one-to-one signal-to-ground ratio |
| | | Doesn't require up-front design effort |
| Disadvantages | Added cost to include connector | More time-consuming to connect |
| | Combination of through-hole and surface-mount | |
| | technology can make signal routing and board component | |
| | loading difficult | |
| | | |

Probing Solutions for 40-pin Logic Analyzers General-Purpose Probing

E5383A 17-channel single-ended flying lead probe

Ideal when only a few lines may need to be probed or probe points are distributed across a target. The E5383A includes a set of 20 IC test clips and five ground leads.

Logic analysis general-purpose probes

General-purpose probing requires connecting probe leads to individual signal lines. This method is most convenient for a small to moderate number of signals, is very flexible, and can be used in conjunction with other probing methods.

Note: Any probed signal line must be able to supply a minimum of 600 mV to the probe with the specified loading.

The standard probing system

The standard probing system consists of IC clips, probe leads, probe housing and probe cable. Because it is passive, the standard probing system is smaller, lighter, and much easier to use than active probing systems. This passive probing system is similar to a probing system used on a high frequency oscilloscope. It consists of an isolation network (as shown in Figure 2.1) at the probe tip and a shielded resistive transmission line. The advantages of this system are:

- High input impedance. See Figure 2.1.
- Signal ground at the probe tip for high-speed signals.
- Inexpensive, removable probe tip assemblies.



Includes logic analyzer

Figure 2.1. Probe tip Isolation network and equivalent load.

Probing Solutions for 40-pin Logic Analyzers General-Purpose Probing (Continued)

Probe leads and lead sets

Probe leads are configured into lead sets, which can probe 16 data channels with ground, one clock channel, and a common ground. A 17-channel probe lead set (E5383A) is shown in Figure 2.2, along with the replacement part numbers for individual components in Figure 2.3.

Each probe lead is a 12-inch, twisted-pair cable connected to the probe cable at the probe housing (see Figure 2.3). The probe tip includes a signal lead, a connector for a ground lead, and the isolation network.

The signal and ground leads can be connected directly to the target system. This requires installing 0.63 mm (0.025 inch) square pins, or round pins with a diameter of between 0.66 mm (0.026 inch) and 0.84 mm (0.033 inch) directly on the board. An IC test clip can also be used. The same specifications apply for the pin dimensions of the test clip. (See Figure 2.6 for IC test clips available from commercial sources.)



Figure 2.2. E5383A 17-channel probe lead set.



Figure 2.3. E5383A 17-channel probe lead set replacement parts.

Probing Solutions for 40-pin Logic Analyzers General-Purpose Probing (Continued)

IC clips

The surface-mount device IC clip with twin hooks (part number 5090-4833, containing 20 IC clips) is designed for fine surface-mounted component leads. The twin hook 0.5 mm IC clip (part number 10467-68701, containing four 0.5 mm IC clips), is very useful for 0.5 mm pitch components. See Figure 2.5.

Grounding

There are three methods of grounding the probe system. First, the entire probe lead set can be grounded through the common ground. This requires only one connection, but is not recommended because it will cause poor signal fidelity in systems with fast transition times. The recommended method is to individually ground each probe lead. This yields optimal signal fidelity and is required for signals with faster transition times (< 4 to 5 ns).

For moderate rise times (greater than 2 ns), it may be acceptable to ground every other (or every fourth) ground connection to the target.



Figure 2.4. Connecting IC clips and ground leads to probes.



Figure 2.5. SMD IC clip and 0.5 mm IC clip.



Figure 2.6. Typical IC test clips available from commercial test clip vendors.

Probing Solutions for 40-pin Logic Analyzers Designing for Logic Analysis Probing

Keysight recommends that targets with probing constraints have connectors designed into the prototype versions of the product for effective hardware and software debug. The following should be considered when designing with connectors:

- Select the appropriate connector technology for your target speed and target density.
- Carefully select all lines for routing to the connectors that may be needed for debug.
- Group the lines at each connector for your probing convenience. For example, Keysight may have written an inverse assembler for your device that has a preconfigured signal order. Before designing, refer to the documentation for this inverse assembler for essential signal lines and order.
- Keep the routing to connectors as short as possible to minimize target impact and provide accurate data.
- Examine the impact of probing isolation networks designed into the target versus the isolation network products offered by Keysight.

An isolation network must be located between the target and the logic analyzer. It can be located on the target board in through-hole or SMT parts. It can also be attached to the logic analyzer cable with the probe leads (the isolation network is molded into the end of the probe); or the Keysight 01650-63203 isolation adapter with self contained isolation networks can be used. Probe leads can be used with connectors but are not the most convenient method. Direct connection of the connectors with the analyzer cable (isolation network parts on the target) or with a probe or isolation adapter is the faster, more convenient method.

High-density, high-performance probes

Keysight Pro Series soft touch connectorless logic analyzer probes

Keysight has developed connectorless logic analyzer probes based on soft touch probing technology. Connectorless logic analyzer probing removes the connector that is traditionally attached to the target board and replaces it with an array of probe pads. This reduces the probe load on the target by eliminating the loading associated with the physical body of the connector. Additionally, this streamlines the design flow by eliminating the need to assign a logic analyzer connector to the bill of material of your board, procuring those connectors and then having them loaded onto your board.

Keysight's soft touch connectorless probes use micro spring-pin technology to provide reliable contact which is not dependent on the planarity of the PC board or the plating processes used to fabricate the board. No special cleaning processes are required when using Keysight's soft touch probes.

The new Keysight Pro Series soft touch connectorless probes offer a 30% smaller footprint than the original soft touch probes and are the basis for the industry standard connectorless probing footprint.

The probes use a retention module that ensures soft touch pin-to-PC board pad alignment and holds the probe in place while in use. The Pro Series soft touch uses a "top-side" mountable retention module. The retention module is mounted on the same side of the board as the probing footprint so there is no need to access the back-side of the board. Because there is no requirement for the retention module pins to extend beyond the back-side of the board, the retention module is compatible with virtually any board thickness.



Figure 3.1. "Top-side" mountable retention module.

E5404A Pro Series soft touch connectorless probe

The E5404A is a 34-channel single-ended Pro Series soft touch connectorless probe compatible with all Keysight logic analyzers that have a 40-pin pod connector. It is capable of acquiring data at the maximum rates of the logic analyzer it is connected to.

Features

- No connector on the target board
- Top-side mount retention module
- Industry-standard connectorless footprint
- 34 channels, single-ended clock and data
- Extremely low, < 0.7 pF, equivalent load capacitance
- Capable of data rates > 2.5 Gb/s (maximum rate dependent on analyzer used)
- 500 mV p-p minimum signal amplitude
- Robust and reliable soft touch technology

Unused clock inputs can be used as data inputs.

The E5404A (used with logic analyzers with a 40-pin cable connector) uses the same footprint, pinout, and retention module as the E5406A Pro Series soft touch connectorless probe (used with logic analyzers with a 90-pin cable connector).

A kit of five retention modules is shipped with each Pro Series soft touch probe. Additional kits can be ordered using Keysight part number E5403A.

E5394A soft touch connectorless probe

The E5394A is a 34-channel single-ended soft touch connectorless probe compatible with all Keysight logic analyzers that have a 40-pin pod connector. It is capable of acquiring data at the maximum rates of the logic analyzer it is connected to. The probe has the following inputs:

- 32 single-ended data inputs
- Two single-ended clock inputs
- < 0.7 pf input capacitance
- 500 mV p-p minimum signal amplitude

Unused clock inputs can be used as data inputs.

The E5394A (used with logic analyzers with a 40-pin pod connector) uses the same footprint, pinout and retention module as the E5390A single-ended soft touch connectorless probe (used with logic analyzers with a 90-pin pod connector).

A kit of five retention modules is shipped with each soft touch probe. Additional kits can be ordered using Keysight part number E5387-68701.



E5396A half-size soft touch connectorless probe

The E5396A is a small space saving probe compatible with all Keysight logic analyzers that have a 40-pin cable connector. It is a 17-channel, singleended probe capable of capturing data at the maximum rates of the logic analyzer it is connected to. The probe has the following inputs:

- 16 single-ended data inputs
- One single-ended clock input
- < 0.7 pf equivalent load capacitance
- 500 mV p-p minimum signal amplitude

The unused clock input can be used as a data input.

The E5396A (used with logic analyzers with a 40-pin cable connector) uses the same footprint, pinout, and retention module as the E5398A single-ended soft touch connectorless probe (used with logic analyzers with a 90-pin cable connector).

More information about soft touch connectorless probes is available on the web at www.keysight.com/find/ softtouch.

Figure 3.2. Keysight E5394A soft touch probe connection.

Probe dimensions

The following figures show dimensions, footprint, and pinout information you will need to design your target system board for use with the Keysight Pro Series soft touch probes.







Figure 3.4. Pro Series soft touch retention module dimensions.

Probe and retention module dimensions

The following dimensions show the Pro Series soft touch probe attached to the retention module. The retention module is mounted on the PC board.



Figure 3.5. Pro Series soft touch side-by-side dimensions.



Figure 3.6. E5394A and E5396A soft touch probe dimensions.

Retention module dimensions

The soft touch probes are attached to the PC board using a retention module which ensures pin-to-pad alignment and holds the probe in place. A board thickness of up to 2.54 mm (0.100 inch) is recommended. Insert the retention module into the board, noting the keying pin, and solder the four alignment pins to the backside of the board.

Probe and retention module

The following dimensions show the soft touch probe attached to the retention

module. The retention module is mounted

dimensions

on the PC board.

34-channel retention module dimensions



17-channel retention module dimensions



Figure 3.7. Retention module dimensions.

34-channel probe and retention module dimensions

25.35 mm 0.998 in. 0.998 in. 2.54 mm 0.100 in. 0.100 in. 8.13 mm 35.05 mm 0.320 in. 1.380 in. Minimum Minimum recommended recommended

17-channel probe and retention module dimensions



Figure 3.8. Side-by-side dimensions.

Drawing notes:

1

- Maintain a solder mask web between pads when traces are routed between the pads on the same layer. The solder mask may not encroach onto the pads within the pad dimension shown.
- VIAs not allowed on these pads. VIA edges may be tangent to pad edges as long as a solder mask web between VIAs and pads is maintained.



- 3 Surface finishes on pads should be HASL immersion silver, or gold over nickel.
- 4 This footprint is compatible with retention module Keysight part number E5405-68702.
- 5 This through hole is not used with the Keysight retention module.
- 6 Plated through hole should not be tied to ground plane for thermal relief.



Figure 3.9. Pro Series soft touch footprint dimensions (see drawing notes).



Figure 3.10. Pad numbers for E5404/06A 34-channel single-ended probes.

| E5404/06A 34-cha single-ended probe | | | Logic anal | yzer | E5404/06A 34-char single-ended probe | nnel | | Logic anal | yzer |
|--|-------|---------------|------------|---------------------|---|-------|---------------|------------|------------------------|
| Signal name | Pad # | ł | Channel | Pod | Signal name | Pad # | | Channel | Pod |
| DO | A1 | \rightarrow | 0 | Whichever pod is | Ground | B1 | | | Whichever pod is |
| D1 | A2 | \rightarrow | 1 | connected to "Odd" | D2 | B2 | \rightarrow | 2 | connected to "Odd" on |
| Ground | A3 | | | on the E5404/06A | D3 | B3 | \rightarrow | 3 | the E5404/06A probe |
| D4 | A4 | \rightarrow | 4 | probe | Ground | Β4 | | | |
| D5 | A5 | \rightarrow | 5 | | D6 | B5 | \rightarrow | 6 | |
| Ground | A6 | | | | D7 | B6 | \rightarrow | 7 | |
| Clock 1+ | A7 | \rightarrow | Clock | _ | Ground | B7 | | | _ |
| GND/NC/Clock 1– | A8 | | | _ | D8 | B8 | \rightarrow | 8 | _ |
| Ground | A9 | | | _ | D9 | B9 | \rightarrow | 9 | _ |
| D10 | A10 | \rightarrow | 10 | _ | Ground | B10 | | | _ |
| D11 | A11 | \rightarrow | 11 | _ | D12 | B11 | \rightarrow | 12 | _ |
| Ground | A12 | | | _ | D13 | B12 | \rightarrow | 13 | _ |
| D14 | A13 | \rightarrow | 14 | _ | Ground | B13 | | | _ |
| D15 | A14 | \rightarrow | 15 | _ | DO | B14 | \rightarrow | 0 | Whichever pod is |
| Ground | A15 | | | Whichever pod is | D1 | B15 | \rightarrow | 1 | connected to "Even" on |
| D2 | A16 | → | 2 | connected to "Even" | Ground | B16 | | | the E5404/06A probe |
| D3 | A17 | \rightarrow | 3 | on the E5404/06A | D4 | B17 | \rightarrow | 4 | |
| Ground | A18 | | | probe | D5 | B18 | \rightarrow | 5 | _ |
| D6 | A19 | \rightarrow | 6 | _ | Ground | B19 | | | _ |
| D7 | A20 | \rightarrow | 7 | _ | GND/NC/Clock 2- | B20 | | | _ |
| Ground | A21 | | | _ | Clock 2+ | B21 | \rightarrow | Clock | _ |
| D8 | A22 | \rightarrow | 8 | _ | Ground | B22 | | | _ |
| D9 | A23 | → | 9 | _ | D10 | B23 | \rightarrow | 10 | _ |
| Ground | A24 | | | _ | D11 | B24 | \rightarrow | 11 | _ |
| D12 | A25 | → | 12 | _ | Ground | B25 | | | _ |
| D13 | A26 | → | 13 | _ | D14 | B26 | \rightarrow | 14 | _ |
| Ground | A27 | | | _ | D15 | B27 | → | 15 | _ |

Probe footprint dimensions

Use these probe footprint dimensions for the PC board pads and holes for attaching the retention module.

Soft touch



NOTES:

- MUST MAINTAIN A SOLDER MASK WEB BETWEEN PADS 4. WHEN TRACES ARE ROUTED BETWEEN THE PADS ON THE SAME LAYER. SOLDERMASK MAY NOT ENCROACH ONTO THE PADS WITHIN THE PAD DIMENSION SHOWN. 5.
- 2. VIA IN PAD NOT ALLOWED ON THESE PADS. VIA EDGES MAY BE TANGENT TO PAD EDGES AS LONG AS A SOLDER MASK WEB BETWEEN VIAS AND PADS IS MAINTAINED.
- 3. PERMISSABLE SURFACE FINISHES ON PADS ARE HASL, IMMERSION SILVER. OR GOLD OVER NICKEL.
- FOOTPRINT IS COMPATIBLE WITH RETENTION MODULE. AGILENT PART ¤E5387-68702.
- RETENTION MODULE DIMENSIONS ARE 34,04 mm x 7,01 mm x 4,98 mm TALL RELATIVE TO THE TOP SURFACE OF THE POB. RETENTION PINS EXTEND 4.32 mm BEYOND THE BOTTOM SURFACE OF THE RM THROUGH THE PCB.
- 6. ASSUME NORMAL ARTWORK TOLERANCES FOR PAD SIZE DIMENSIONS

Half-size soft touch



NOTES:

- MUST MAINTAIN A SOLDER MASK WEB BETWEEN PADS WHEN TRACES ARE ROUTED BETWEEN THE PADS ON THE SAME LAYER, SOLDERMASK MAY NOT ENCROACH ONTO THE PADS WITHIN THE PAD DIMENSION SHOWN.
- 2 VIA IN PAD NOT ALLOWED ON THESE PADS, VIA EDGES MAY BE TANGENT TO PAD EDGES AS LONG AS A SOLDER MASK WEB BETWEEN VIAS AND PADS IS MAINTAINED.
- 3. PERMISSABLE SURFACE FINISHES ON PADS ARE HASL. IMMERSION SILVER. OR GOLD OVER NICKEL.
- 4. FOOTPRINT IS COMPATIBLE WITH RETENTION MODULE. AGILENT PART NUMBER E5396-68702.
- 6. RETENTION MODULE DIMENSIONS ARE 020.04 mm x 6.99 mm x 4.95 mm TALL RELATIVE TO THE TOP TOP SURFACE OF THE PCB. RETENTION PINS EXTEND 27.48 mm BEYOND THE BOTTOM SURFACE OF THE RM THROUGH THE PCB.
- Figure 3.11. Footprint dimensions.

Pinout for the E5394A single-ended soft touch probe

The following graphic and table show the E5394A single-ended soft touch probe pad numbers and logic analyzer pod inputs.

| POD 1 | POD 2 |
|---|--|
| $\begin{array}{c ccccccccccccccccccccccccccccccccccc$ | $ \begin{array}{c c} & & & & & & & & \\ \hline & & & & & \\ & & & &$ |
| | |
| POD 1 | POD 2 |

Figure 3.12. Pinout.

| E5394A single-ended probe | | | | | E5394A single-er | E5394A single-ended probe L | | | Logic analyzer | | |
|---------------------------|-------|---------------|---------|-----------------------|------------------|-----------------------------|----------|---------|-------------------------|--|--|
| Signal name | Pad # | | Channel | Pod | Signal name | Pad # | | Channel | Pod | | |
| D1 | A1 | \rightarrow | 1 | Whichever pod is | DO | B1 | → | 0 | Whichever pod is | | |
| D3 | A2 | \rightarrow | 3 | connected to "Odd" on | D2 | B2 | → | 2 | _ connected to "Odd" or | | |
| Ground | A3 | | | _ the E5394A probe | Ground | B3 | | - | the E5394A probe | | |
| D5 | A4 | \rightarrow | 5 | _ | D4 | B4 | → | 4 | | | |
| D7 | A5 | \rightarrow | 7 | _ | D6 | B5 | → | 6 | _ | | |
| Ground | A6 | | | _ | Ground | B6 | | 0 | _ | | |
| D9 | Α7 | \rightarrow | 9 | _ | D8 | B7 | → | 8 | _ | | |
| D11 | A8 | \rightarrow | 11 | _ | D10 | B8 | → | 10 | _ | | |
| Ground | A9 | | | _ | Ground | B9 | | | _ | | |
| D13 | A10 | \rightarrow | 13 | _ | D12 | B10 | → | 12 | _ | | |
| D15 | A11 | \rightarrow | 15 | _ | D14 | B11 | → | 14 | _ | | |
| Ground | A12 | | | _ | Ground | B12 | | | _ | | |
| NC | A13 | \rightarrow | NC | | Clock | B13 | → | Clock | _ | | |
| Ground | A14 | | | Whichever pod is | Ground | B14 | | | Whichever pod is | | |
| D1 | A15 | \rightarrow | 1 | connected to "Even" | DO | B15 | → | 0 | connected to "Even" | | |
| D3 | A16 | \rightarrow | 3 | on the E5394A probe | D2 | B16 | → | 2 | on the E5394A probe | | |
| Ground | A17 | | | _ | Ground | B17 | | | _ ' | | |
| D5 | A18 | \rightarrow | 5 | _ | D4 | B18 | <i>→</i> | 4 | _ | | |
| D7 | A19 | \rightarrow | 7 | _ | D6 | B19 | → | 6 | _ | | |
| Ground | A20 | | | _ | Ground | B20 | | | _ | | |
| D9 | A21 | \rightarrow | 9 | _ | D8 | B21 | <i>→</i> | 8 | _ | | |
| D11 | A22 | \rightarrow | 11 | _ | D10 | B22 | → | 10 | _ | | |
| Ground | A23 | | | _ | Ground | B23 | | | _ | | |
| D13 | A24 | \rightarrow | 13 | _ | D12 | B24 | → | 12 | _ | | |
| D15 | A25 | \rightarrow | 15 | _ | D14 | B25 | → | 14 | _ | | |
| Ground | A26 | | | _ | Ground | B26 | | | _ | | |
| NC | A27 | \rightarrow | NC | | Clock | B27 | → | Clock | _ | | |

Pinout for the E5396A 17-channel single-ended soft touch probe

The following graphic and table show the E5396A single-ended soft touch probe pad numbers and logic analyzer pod inputs.

| B1 | 000000000000000000000000000000000000000 | 0 B13 |
|------------|---|------------|
| \bigcirc | D0 D2 G D4 D6 G D8 D10 G D12 D14 G CLK | |
| A1 | 000000000000000000000000000000000000000 | A13 |
| | D1 D3 G D5 D7 G D9 D11 G D13 D15 G NC | \bigcirc |

Figure 3.13. Pinout.

| E5396A 17-channel single-ended probe | | | Logic analy: | zer |
|--------------------------------------|-------|---------------|--------------|------------------|
| Signal name | Pad # | | Channel | Pod |
| D1 | A1 | \rightarrow | 1 | Whichever pod is |
| D3 | A2 | \rightarrow | 3 | plugged into the |
| Ground | A3 | | | E5396A probe |
| D5 | A4 | → | 5 | - |
| D7 | A5 | → | 7 | |
| Ground | A6 | | | |
| D9 | A7 | → | 9 | |
| D11 | A8 | → | 11 | |
| Ground | A9 | | | |
| D13 | A10 | → | 13 | |
| D15 | A11 | → | 15 | |
| Ground | A12 | | | |
| NC | A13 | → | n/a | - |

| E5396A 17-channel single-ended probe | | | Logic analy | zer |
|--------------------------------------|-------|---------------|-------------|------------------|
| Signal name | Pad # | | Channel | Pod |
| DO | B1 | \rightarrow | 0 | Whichever pod is |
| D2 | B2 | \rightarrow | 2 | plugged into the |
| Ground | B3 | | | E5396A probe |
| D4 | B4 | \rightarrow | 4 | - |
| D6 | B5 | \rightarrow | 6 | _ |
| Ground | B6 | | | - |
| D8 | B7 | \rightarrow | 8 | |
| D10 | B8 | \rightarrow | 10 | _ |
| Ground | B9 | | | - |
| D12 | B10 | \rightarrow | 12 | - |
| D14 | B11 | \rightarrow | 14 | - |
| Ground | B12 | | | - |
| Clock | B13 | → | Clock | - |

Equivalent probe loads

The following probe load models are based on in-circuit measurements made with a Keysight 8753E 6 GHz network analyzer and a Keysight 54750A TDR/TDT using a 50 Ω test fixture. The following schematic accurately models the probe load out to 6 GHz.



Figure 3.14. Simple (does not include capacitive coupling between channels or inductance of the spring pins).



Figure 3.15. Complex (includes capacitive coupling between channels and inductance of spring pins).

High-Density, High-Performance

Keysight has developed high-density probing solutions based on the 100-pin Samtec and AMP Mictor 38-pin connectors. The Keysight probes and adapter cables, E5346A, E5339A, E5351A, and E5385A, provide a connection strategy to route your important signals to the Keysight logic analyzer. Simply design the connectors onto the board for the critical signals such as address, data, and status bits. The connectors consume a minimal amount of board space. Each connector provides 32 channels of logic analysis per connector and two clocks (unused clocks can be used as data). Connectors for use with the E5385A, E5346A, E5339A, and E5351A can be purchased directly from AMP, Samtec, or Keysight. See the "Related Information" at the end of this document.



Figure 3.16. E5385A Samtec 100-pin probe mechanical dimensions.



Figure 3.17. E5346A, E5351A, E5339A Mictor probes mechanical dimensions.

Keysight Technologies E5346A, E5339A, and E5385A probes

The E5346A, E5339A, and E5385A probes include the required isolation networks for the logic analyzer right at the probe tip, close to the target. The E5346A and E5385A are designed to acquire signals with peak-to-peak amplitude as low as 500 mV. The E5339A is designed to acquire signals as small as 250 mV peak-to-peak. Figure 3.18 shows the equivalent load for the E5339A, and Figure 3.19 shows the equivalent load for the E5346A. Figure 3.20 shows the equivalent load for the E5385A.

To use the E5346A, E5339A, or E5385A at high clock speeds, the following design guidelines should be observed:

- Calculate the electrical length of the probe hookup stub.
- For PC board material with E_r =4.9, use a propagation delay of 160 ps/inch.
- Check that the propagation delay of the probe hookup stub is less than 20% of the bus signal rise time (T_r). If it is, the E5346A, E5339A, or E5385A can be used for connection.

For example, if E_r =4.9, a 2.5 inch probe hookup stub generates a propagation delay of 400 ps. If T_r is > 2 ns, the E5346A, E5339A, or E5385A is a viable probing choice.

The E5346A and E5339A use the AMP Mictor 38-pin connector. The E5385A uses a 100-pin connector manufactured by Samtec. Keysight recommends the E5394A or E5385A for new applications, due to the reduced input capacitive loading and improved isolation between adjacent channels.



Figure 3.18. E5339A Low Voltage Mictor probe input equivalent load.









Figure 3.20. E5385A Samtec probe input equivalent load

For additional information on designing connectors into a target system, refer to the following documents:

| Keysight E5346A/E5351A Probe/Adapter | Installation note E5346-92014 | http://literature.keysight.com/litweb/pdf/E5346-92014.pdf | |
|--------------------------------------|-------------------------------|---|--|
| Cable | | | |
| Keysight E5339A Low Voltage Probe | Installation note E5339-92002 | http://literature.keysight.com/litweb/pdf/E5339-92002.pdf | |
| Keysight E5385A Probe | Installation note E5385-92001 | http://literature.keysight.com/litweb/pdf/E5385-92001.pdf | |



Figure 3.21. Keysight E5339A, E5346A, and E5351A connection and pinout.



Figure 3.22. Keysight E5339A, E5346A, and E5385A design rules.



Figure 3.23. Keysight E5385A connection and pinout

E5385A 100-pin probe pin assignments

| Signal | Pin nı | ımber | Signal |
|------------|-----------------|-------|------------|
| Ground | 1 | 2 | Ground |
| Do Not | 3 | 4 | Do Not |
| Connect | | | Connect |
| Ground | 5 | 6 | Ground |
| Odd D0 | 7 | 8 | Even D0 |
| Ground | 9 | 10 | Ground |
| Odd D1 | 11 | 12 | Even D1 |
| Ground | 13 | 14 | Ground |
| Odd D2 | 15 | 16 | Even D2 |
| Ground | 17 | 18 | Ground |
| Odd D3 | 19 | 20 | Even D3 |
| Ground | 21 | 22 | Ground |
| Odd D4 | 23 | 24 | Even D4 |
| Ground | 25 | 26 | Ground |
| Odd D5 | 27 | 28 | Even D5 |
| Ground | 29 | 30 | Ground |
| Odd D6 | 31 | 32 | Even D6 |
| Ground | 33 | 34 | Ground |
| Odd D7 | 35 | 36 | Even D7 |
| Ground | 37 | 38 | Ground |
| Odd D8 | 39 | 40 | Even D8 |
| | 41 | 40 | |
| Ground | | | Ground |
| Odd D9 | 43 | 44 | Even D9 |
| Ground | 45 | 46 | Ground |
| Odd D10 | 47 | 48 | Even D10 |
| Ground | 49 | 50 | Ground |
| Odd D11 | 51 | 52 | Even D11 |
| Ground | 53 | 54 | Ground |
| Odd D12 | 55 | 56 | Even D12 |
| Ground | 57 | 58 | Ground |
| Odd D13 | 59 | 60 | Even D13 |
| Ground | 61 | 62 | Ground |
| Odd D14 | 63 | 64 | Even D14 |
| Ground | 65 | 66 | Ground |
| Odd D15 | 67 | 68 | Even D15 |
| Ground | 69 | 70 | Ground |
| NC | 71 | 72 | NC |
| Ground | 73 | 74 | Ground |
| NC | 75 | 76 | NC |
| Ground | 77 | 78 | Ground |
| Odd D16P/ | 79 | 80 | Even D16P/ |
| Odd CLK | | | Even CLK |
| Ground | 81 | 82 | Ground |
| NC | 83 | 84 | NC |
| Ground | 85 | 86 | Ground |
| NC | 87 | 88 | NC |
| Ground | 89 | 90 | Ground |
| NC | 91 | 92 | NC |
| Ground | 93 | 94 | Ground |
| Ground | 95 | 96 | Ground |
| +5V | <u>95</u> 97 | 90 | +5V |
| +5V +5V | | 98 | +5V +5V |
| | 99 | 100 | |

Keysight E5351A 38-pin adapter cable

If the calculated electrical length of the required routing stub prohibits the use of the Keysight E5339A, E5346A, or E5385A, the Keysight E5351A can be used with the required isolation networks installed on the target.

The E5351A does not have its own internal isolation networks. When using the E5351A, place the SIP isolation networks, surface mount isolation network 5062-7396, or equivalent discrete components very near the target component for measurement. Ensure that the stub length between the target component and the isolation network is short. The stub propagation delay should be less than 20% of the bus signal rise time, as mentioned before. The transmission line from the on-board isolation network to the Mictor connector should be designed for an impedance in the range of 80 to 100 ohms (closer to 100 ohms is better). This length should not exceed 3 to 4 inches, and all signal line lengths should be equal. Signal line length variation should not cause propagation delay variation to exceed 20 ps between signal lines.

Notes on using discrete components

Discrete components can be used in the design of the RC network. Keysight recommends the circuit shown in Figure 3.25. To achieve the equivalent load shown in the figure, trace lengths should be minimized by locating the RC network very near the measured node. Actual load will be the stub length load added to the equivalent load in the figure.



Figure 3.24. Keysight E5351A design rules.

Options for on-board terminations for the E5351A

There are two options for isolating the E5351A on the target PC board:

- Use the surface mount isolation network, Keysight part number 5062-7396. Refer to Figure 3.26 for schematic and pinout.
- Use discrete components. Refer to Figure 3.25 for recommended components and equivalent load.

If you are operating at state speeds above 200 MHz, you should use discrete components for best results. Due to the added electrical length of the E5351A probe cable, the divider compensating capacitors in the SIP, and surface-mount isolation networks are not optimum for the E5351A, but they are usable up to 200 MHz clock rates.

Notes on using the 5062-7396 SMT part

Keysight currently recommends a twostep process in soldering the SMT part to the board. The first pass places solder paste on those pads with vias. Application of heat allows the via to fill with solder. (If only one solder step is used, the solder wicks away from the part into the via and a solid connection will not be made with the part.) The next pass places solder paste on all of the pads.

As shown in Figure 3.26, the 5062-7396 SMT isolation network supports six logic analysis channels. The size of the part allows you to repeat the pattern in Figure 3.26 to accommodate multiple parts stacked end-to-end for the number of channels needed in your application. Three of these SMTs are required for each probe cable. The process for using the ceramic hybrid isolation network is similar to the process for an LCC package. Due to the small part size, thermal expansion mismatch during solder reflow should not be a problem. Capacitance also remains stable with temperature changes.





Includes on board RC network and logic analyzer

Figure 3.25. Suggested on-board isolation network and equivalent load when using discrete components to terminate the E5351A.

- The effective input capacitance for on-board isolation networks is purely a function of geometry 0.3 pF is about as low as can be achieved.
- 2. The equivalent load is the same when using the surface-mount isolation network, 5062-7396.



Logic analyzer pod pad dimension = 0.030" x 0.040" Resistances: R1 through R6: 250 Ω R7 through R12: 90.9 k Ω Capacitance 8.2 pF

Figure 3.26. Recommended PC board pattern for 5062-7396 surface mount isolation network.

Support shrouds

A support shroud is recommended to provide additional strain relief between the probe and the connector, as shown in Figures 3.21 and 3.23. Two plated through-holes are required on the target board. The shroud is mounted directly to the target board using the throughholes. This places the shroud around the connector, providing solid mechanical strain relief. Connector kits are available; Table 1 shows the Keysight part numbers for shrouds and connector kits for various PC board thicknesses.

Table 1. Mating connectors, shrouds, and kits for Keysight E5339A, E5346A, E5351A, and E5385A probes

| For probe model numbers | Description | Keysight part number |
|----------------------------|--|-------------------------|
| E5339A, E5346A, E5351A | Kit of five support shrouds and five 38-pin Mictor connectors for PC board thickness up to 1.57 mm (0.062") | E5346-68701 |
| | Kit of five support shrouds and five 38-pin Mictor connectors for PC board thickness up to 3.175 mm (0.125") | E5346-68700 |
| | One 38-pin Mictor connector (also available from AMP as part number 2-767004-2) | 1252-7431 |
| | One support shroud for PC board thickness up to 1.57 mm (0.062") | E5346-44701 |
| | One support shroud for PC board thickness up to 3.175 mm (0.125") | E5346-44704 |
| | One support shroud for PC board thickness up to 4.318 mm (0.700") | E5346-44703 |
| E5385A | Kit of five support shrouds and five 100-pin Samtec connectors for PC board thickness up to 1.57 mm (0.062") | 16760-68702 |
| | Kit of five support shrouds and five 100-pin Samtec connectors for PC board thickness up to 3.05 mm (0.120") | 16760-68703 |
| | One 100-pin Samtec connector (also available from Samtec as part number ASP-65067-01) | 1253-3620 |
| | One support shroud for PC board thickness up to 1.57 mm (0.062") | 16760-02302 |
| | One support shroud for PC board thickness up to 3.05 mm (0.120") | 16760-02303 |



Figure 3.27. Mechanical information for E5346-44701, E5346-44703, E5346-44704 support shrouds for 38-pin Mictor connectors.

Right-angle Mictor adapter

For systems with space constraints above the 38-pin connector, Keysight offers a right-angle adapter, as shown in Figure 4.1. With the E5346-63201 right-angle adapter inserted in the 38-pin connector, the adapter cable is connected parallel to the target board surface. When using the right-angle adapters, the 38-pin connectors must be placed end-to-end on the target board, as shown in Figure 4.2. Support shrouds cannot be used with the right-angle adapter.







Figure 4.1. E5346-63201 right-angle 38-pin adapter.



Figure 4.2. 38-pin connectors placed for use of right-angle adapter.

Note: The right-angle adapter adds significant capacitance and inductance in series with the probe. It is not recommended for state speeds above 100 MHz or for signals with rise times < 4 to 5 ns.

Low density, moderate performance solutions shown in the "High-Density, High-Performance" (page 23) section of this document can be used in place of the solutions described here. Keysight recommends standard 0.1 inch center connectors for normal density applications if the loading/speed is not a significant issue. Many of these items are available from 3M or Keysight (see Table 2). See the "Related Information" section at the end of this document for 3M address information.

Direct connection through isolation adapter

Isolation adapters (Keysight part number 01650-63203) that connect to the end of the probe cable are designed to perform two functions. The first is to reduce the number of pins required for the header on the target board from 40 pins to 20 pins. This process reduces the board area dedicated to the probing connection. The second function is to provide the proper RC networks in a very convenient package. Figure 4.3 illustrates how the isolation adapter physically connects to the target system and the equivalent load of the isolation adapter connected to a Keysight logic analyzer. Figures 4.4 and 4.5 show the pinout diagrams for the probe cable and the isolation adapter, respectively. There are two 20-pin connectors, along with their Keysight and 3M part numbers, listed in Table 2.

Table 2. Twenty-pin connectors for fixed configuration probing. (Requires isolation adapter)

| Keysight part number | 3M part number | Connector description |
|----------------------|----------------|-----------------------------------|
| 1251-8106 | 2520-6002 | 20-pin, low-profile (straight) |
| 1251-8473 | 2520-5002 | 20-pin, low-profile (right-angle) |



Includes logic analyzer

Figure 4.3. Isolation adapter (01650-63203) and equivalent load.

 The Keysight 01650-63203 saves space by using a common ground (see Figure 4.5). This will impact signal fidelity, especially faster transition times (< 4 to 5 ns).



Figure 4.4. Pinout for probe cable.



Figure 4.5. Pinout for 100 k Ω isolation adapter (Keysight part number 01650-63203).

1. +5 V is supplied from the logic analyzer to provide power for analysis probes and demo boards. **DO NOT connect these pins to a +5 V supply in the target system!**

Direct connection through 40-pin connectors

The probe cable also can be plugged directly into the various 40-pin connectors shown in Table 3, but proper isolation networks must be installed directly onto the target system board (see Figure 4.6 for the 40-pin connector pinout).

Keysight offers a 12-pin SMT (Keysight part number 5062-7396), which provides six isolation networks, as shown in Figure 4.7. Three of these SMTs are required for each probe cable.

Discrete components can also be used for the proper isolation network. See Figure 4.9 for an equivalent load diagram for the isolation networks.

Note that the effective input capacitive lead of an isolation network using discrete components is a function of the layout geometry and the parasitic capacitance of the input series damping resistor.

Table 3. Forty-pin connectors for fixed configuration probing. (Requires isolation network installed on target board)

| Keysight part number | 3M part number | Connector description |
|----------------------|----------------|---|
| 1251-8158 | 2540-5002 | 40-Pin, low-profile (right-angle) |
| 1251-8831 | 3432-6302 | 40-Pin, with long latches (straight) |
| 1251-8931 | 3432-5302 | 40-Pin, with long latches (right-angle) |

Table 4. Available isolation networks

| Keysight part number | Package type |
|----------------------|---|
| 5062-7396 | SMT, 12-pin, provides 6 isolation networks (3 SMTs required for each probe cable) |

Figure 4.6. Forty-pin connector pinout.

1. +5 V is supplied from the logic analyzer to provide power for analysis probes and demo boards. **DO NOT connect these pins to a +5 V supply in the target system!**



Logic analyzer pod pad dimension = 0.030" x 0.040"

- 1. Resistances:
- R1 through R6: 250 Ω
- R7 through R12: 90.9 kΩ
- 2. Capacitance 8.2 pF

Figure 4.7. Recommended PC board pattern for 5062-7396 surface mount isolation network.



Figure 4.8. Connecting probe cable to 40-pin connector with isolation networks.

Notes on using discrete components

Discrete components can be used to design the isolation network. Keysight recommends the circuit shown in Figure 4.9. To achieve the equivalent load shown in the figure, trace lengths should be minimized by locating the RC network very near the measured node. Actual load will be the stub length load added to the equivalent load in the figure. Trace length from the suggested on-board RC network to the target connector must be 3 to 4 inches or less. This transmission line should be designed for an impedance in the range of 80 to 100 ohms (closer to 100 ohms is better).







Figure 4.9. Equivalent load for on-target discrete components. Also applies to SMT (5062-7396) RC networks.
E5382A single-ended flying lead probe set

The E5382A is a 17-channel single-ended flying lead probe compatible with logic analyzers with a 90-pin pod connection. It is capable of acquiring data at the maximum rates of the logic analyzer it is connected to. The E5382A is useful for acquiring signals from dispersed locations or when a mass connection scheme is not available. The E5382A has the following:

- 16 single-ended data inputs
- One differential or single-ended clock input
- Variety of supplied accessories

Unused clock inputs can be used as data inputs.

Table 5. Accessories





Figure 5.1. E5382A flying lead set.

Suggested configurations and characteristics

Table 6. E5382A suggested configurations and characteristics

| Configuration | Description | Total lumped input C | Maximum recommended state speed |
|---------------------------------------|---|----------------------|---------------------------------|
| | 130 Ω resistive signal pin (orange) and solder- down ground lead | 1.3 pF | 1.5 Gb/s |
| A A A A A A A A A A A A A A A A A A A | 5 cm resistive signal lead (can be soldered- down) and solder-down ground lead | 1.6 pF | 1.5 Gb/s |
| | Flying lead and ground extender | 1.4 pF | 1.5 Gb/s |
| | Grabber clip and right-angle 2.0 pf ground lead | 2.0 pF | 600 Mb/s |

Available accessories

NOTE: Examples of convenient connection which may result in degraded performance

Ground connector

It is essential to ground every tip that is in use. For best performance at high speeds, every tip should be grounded individually to ground in the system under test.

Adapting to coaxial connectors

The Keysight E9638A probe tip to BNC adapter can be used to connect one of the flying lead probes of the E5382A to a BNC connector. To probe other coaxial connectors, use the E9638A adapter, a BNC termination, and an adapter to the other type of coaxial connector. Refer to Figure 5.3.



Figure 5.2. 5063-2174 BNC to probe tip adapter.





E5381A differential flying-lead probe set

The E5381A is a 17-channel differential flying-lead probe compatible with logic analyzers with a 90-pin pod connection. It is capable of acquiring data at the maximum rates of the logic analyzer it is connected to. The E5381A is useful for acquiring signals from dispersed locations or when a mass connection scheme is not available. The E5381A has the following:

- 16 differential or single-ended data inputs
- One differential or single-ended clock input
- Variety of supplied accessories

Unused clock inputs can be used as data inputs.



Figure 5.4. E5381A differential flying-lead probe set accessories.

Replaceable parts and additional accessories

| Description | Quantity | Keysight part number |
|--|----------|----------------------|
| 82 Ω resistor trimming template | 1 | 01131-94309 |
| Accessory kit - coaxial tip resistors (82 Ω) | 34 | E5381-82101 |
| Accessory kit - socket adapter | 34 | E5381-82102 |
| Accessory kit - damped wire (160 Ω) | 34 | E5381-82103 |
| Accessory kit - 3-pin header | 34 | E5381-82104 |
| Cable - main | 1 | E5381-61601 |



Figure 5.5. E5381A differential flying-lead probe set.

Suggested configurations and characteristics

Table 7. E5381A suggested configurations and characteristics

| Configuration | Description Coaxial tip Resistor (82 Ω blue) Solder attach to components, traces, pads, or VIAs. | Total lumped input C 0.9 pF | Maximum recommended state speed 1.5 Gb/s |
|---------------|--|---------------------------------------|---|
| | 3-pin header | 1.0 pF | 1.5 Gb/s |
| | Socket adapter | 1.1 pF | 1.5 Gb/s |
| | Damped wire Solder attach to components, traces, pads, or VIAs. | 1.3 pF | 1.5 Gb/s |

Recommended probe configurations

For the best performance, use the following configurations. The configurations are listed in the recommended order.

Recommended probe configurations

For the best performance, use the following configurations. The configurations are listed in the recommended order.



Figure 5.6. Probing configurations that give the best signal fidelity.

Seven options are available for connecting Keysight logic analyzers with 90-pin pod connectors to a target system using mass connections.

Keysight Pro Series soft touch connectorless logic analyzer probes

Keysight has developed connectorless logic analyzer probes based on soft touch probing technology. Connectorless logic analyzer probing removes the connector that is traditionally attached to the target board and replaces it with an array of probe pads. This reduces the probe load on the target by eliminating the loading associated with the physical body of the connector. Additionally, this streamlines the design flow by eliminating the need to assign a logic analyzer connector to the bill of material of your board, procuring those connectors and then having them loaded onto your board.

Keysight's soft touch connectorless probes use micro spring-pin technology to provide reliable contact which is not dependent on the planarity of the PC board or the plating processes used to fabricate the board. No special cleaning processes are required when using Keysight's soft touch probes.

The new Keysight Pro Series soft touch connectorless probes offer a 30% smaller footprint than the original soft touch probes and are the basis for the industry standard connectorless probing footprint.

The probes use a retention module that ensures soft touch pin-to-PC board pad alignment and holds the probe in place while in use. The Pro Series soft touch uses a "top-side" mountable retention module. The retention module is mounted on the same side of the board as the probing footprint so there is no need to access the back-side of the board. Because there is no requirement for the retention module pins to extend beyond the back-side of the board, the retention module is compatible with virtually any board thickness.



Figure 6.1. "Top-side" mountable retention module.

E5405A Differential Pro series soft touch connectorless probe

The E5405A is a 17-channel differential Pro Series soft touch connectorless probe compatible with all Keysight logic analyzers that have a 90-pin pod connector. It is capable of acquiring data at the maximum rates of the logic analyzer it is connected to.

Features

- No connector on the target board
- Top-side retention module
- Industry-standard connectorless footprint
- 17 channels, differential or singleended clock and data
- Extremely low, < 0.7 pF, equivalent load capacitance
- Capable of data rates > 2.5 Gb/s (maximum rate dependent on analyzer used)
- 200 mV Vmax-Vmin minimum signal amplitude
- Robust and reliable soft touch technology

Unused clock inputs can be used as data inputs.

The E5405A uses the same retention module as the E5404A and E5406A Pro Series soft touch connectorless probe.

A kit of five retention modules is shipped with each Pro Series soft touch probe. Additional kits can be ordered using Keysight part number E5403A.

E5406A/E5402A Pro Series soft touch connectorless probes

The E53406A/E5402A are 34-channel single-ended Pro Series soft touch connectorless probe compatible with all Keysight logic analyzers that have a 90pin pod connector. The E5402A is a low profile right angle version of the E5406A probe.

Features

- No connector on the target board
- Top-side mount retention module
- Industry-standard connectorless footprint
- 34 channels, single-ended or differential clock and single-ended data
- Extremely low, < 0.7 pF, equivalent load capacitance
- Capable of data rates > 2.5 Gb/s (maximum rate dependent on analyzer used)
- 250 mV p-p minimum signal amplitude
- Robust and reliable soft touch technology

Unused clock inputs can be used as data inputs.

The E5406A (used with logic analyzers with a 90-pin cable connector) uses the same footprint, pinout, and retention module as the E5404A and E5402A Pro Series soft touch connectorless probes (used with logic analyzers with a 40-pin cable connector).

A kit of five retention modules is shipped with each Pro Series soft touch probe. Additional kits can be ordered using Keysight part number E5403A. The low profile E5402A probe uses retention module Keysight part number E5412A.

E5387A Differential soft touch connectorless probe

The E5387A is a 17-channel differential soft touch connectorless probe compatible with all Keysight logic analyzers that have a 90-pin pod connector. It is capable of acquiring data at the maximum rates of the logic analyzer it is connected to. The probe has the following inputs:

- 16 differential or single-ended data inputs
- One differential or single-ended clock input
- < 0.7 pf input capacitance
- 200 mV V_{max}-V_{min} minimum signal amplitude

Unused clock inputs can be used as data inputs.

The E5387A uses the same retention module as the E5390A and E5394A soft touch probes.

A kit of five retention modules is shipped with each soft touch probe. Additional kits can be ordered using Keysight part number E5387-68701.

E5390A single-ended soft touch connectorless probe

The E5390A is a 34-channel singleended soft touch connectorless probe compatible with all Keysight logic analyzers that have a 90-pin pod connector. It is capable of acquiring data at the maximum rates of the logic analyzer it is connected to. The probe has the following inputs:

- 32 single-ended data inputs
- Two differential or single-ended clock inputs
- < 0.7 pf input capacitance
- 250 mV p-p minimum signal amplitude

Unused clock inputs can be used as data inputs.

The E5390A (used with logic analyzers with a 90-pin pod connector) uses the same footprint, pinout and retention module as the E5394A single-ended soft touch connectorless probe (used with logic analyzers with a 40-pin pod connector).

A kit of five retention modules is shipped with each soft touch probe. Additional kits can be ordered using Keysight part number E5387-68701.

E5398A half-size soft touch connectorless probe

The E5398A is a small space saving probe compatible with all Keysight logic analyzers that have a 90-pin cable connector. It is a 17-channel, single-ended probe capable of capturing data at the maximum rates of the logic analyzer it is connected to. The probe has the following inputs:

- 16 single-ended data inputs
- One differential or single-ended clock input
- < 0.7 pf equivalent load capacitance
- 250 mV p-p minimum signal amplitude

Unused clock inputs can be used as data inputs.

The E5398A (used with logic analyzers with a 90-pin cable connector) uses the same footprint, pinout, and retention module as the E5396A single-ended soft touch connectorless probe (used with logic analyzers with a 40-pin cable connector).

More information about soft touch connectorless probes is available on the web at www.keysight.com/find/softtouch.



Figure 6.2. Soft touch probes.

Probe dimensions

The following figures show dimensions, footprint, and pinout information you will need to design your target system board for use with the Keysight soft touch probes.







Figure 6.4. E5406A probe dimensions.



Figure 6.5. Pro Series soft touch retention module dimensions, part number E5403A.

Pro Series soft touch retention module dimensions

The following dimensions show the soft touch probe attached to the retention module. The retention module is mounted on the PC board.



Figure 6.6. Pro Series soft touch side-by-side dimensions with retention module, part number E5403A.



Figure 6.7. E5402A probe dimensions.



Figure 6.8. E5412A retention module dimensions.



Figure 6.9. E5412A side-by-side dimensions.

Drawing notes:

- 1 Maintain a solder mask web between pads when traces are routed between the pads on the same layer. The solder mask may not encroach onto the pads within the pad dimension shown.
- VIAs not allowed on these pads. VIA edges may be tangent to pad edges as long as a solder mask web between VIAs and pads is maintained.



- 3 Surface finishes on pads should be HASL immersion silver, or gold over nickel.
- 4 This footprint is compatible with retention module Keysight part number E5405-68702.
- 5 This through hole is not used with the Keysight retention module.
- 6 Plated through hole should not be tied to ground plane for thermal relief.



Figure 6.10. Pro Series soft touch footprint dimensions (see drawing notes).



Figure 6.11. Pad numbers for E5404/06A 34-channel single-ended probes.

| | 5404/06A 34-channel single- Logic analyzer ended probe | | /zer | E5404/06A 34-char ended probe | nnel sing | jle- | Logic analyzer | | |
|-----------------|---|---------------|---------|----------------------------------|-----------------|-------|----------------|---------|------------------------|
| Signal name | Pad # | | Channel | Pod | Signal name | Pad # | | Channel | Pod |
| DO | A1 | \rightarrow | 0 | Whichever pod is | Ground | B1 | | | Whichever pod is |
| D1 | A2 | → | 1 | connected to "Odd" on | D2 | B2 | → | 2 | connected to "Odd" on |
| Ground | A3 | | | the E5404/06A probe | D3 | В3 | \rightarrow | 3 | the E5404/06A probe |
| D4 | A4 | → | 4 | _ | Ground | B4 | | | _ |
| D5 | A5 | → | 5 | | D6 | B5 | \rightarrow | 6 | |
| Ground | A6 | | | | D7 | B6 | \rightarrow | 7 | |
| Clock 1+ | Α7 | → | Clock | _ | Ground | B7 | | | _ |
| GND/NC/Clock 1– | A8 | | | - | D8 | B8 | \rightarrow | 8 | - |
| Ground | A9 | | | | D9 | B9 | \rightarrow | 9 | |
| D10 | A10 | Ŷ | 10 | _ | Ground | B10 | | | _ |
| D11 | A11 | → | 11 | _ | D12 | B11 | \rightarrow | 12 | _ |
| Ground | A12 | | | | D13 | B12 | \rightarrow | 13 | |
| D14 | A13 | → | 14 | | Ground | B13 | | | |
| D15 | A14 | → | 15 | | DO | B14 | \rightarrow | 0 | Whichever pod is |
| Ground | A15 | | | Whichever pod is | D1 | B15 | \rightarrow | 1 | connected to "Even" on |
| D2 | A16 | \rightarrow | 2 | connected to "Even" | Ground | B16 | | | the E5404/06A probe |
| D3 | A17 | \rightarrow | 3 | on the E5404/06A | D4 | B17 | \rightarrow | 4 | |
| Ground | A18 | | | probe | D5 | B18 | \rightarrow | 5 | |
| D6 | A19 | \rightarrow | 6 | _ | Ground | B19 | | | |
| D7 | A20 | → | 7 | | GND/NC/Clock 2- | B20 | | | |
| Ground | A21 | | | _ | Clock 2+ | B21 | \rightarrow | Clock | _ |
| D8 | A22 | → | 8 | | Ground | B22 | | | |
| D9 | A23 | → | 9 | | D10 | B23 | \rightarrow | 10 | |
| Ground | A24 | | | _ | D11 | B24 | \rightarrow | 11 | _ |
| D12 | A25 | \rightarrow | 12 | _ | Ground | B25 | | | _ |
| D13 | A26 | \rightarrow | 13 | _ | D14 | B26 | \rightarrow | 14 | _ |
| Ground | A27 | | | _ | D15 | B27 | → | 15 | _ |

Figure 6.12. Pad numbers for E5405A 17-bit differential probe.

| E5405A different | tial probe | Logic anal | yzer | E5405A different | ial probe | Logic analy | zer |
|------------------|------------|------------|------------------|------------------|-----------|---------------------------|------------------|
| Signal name | Pad # | Channel | Pod | Signal name | Pad # | Channel | Pod |
| D0 (+) | A1 → | 0 | Whichever pod is | Ground | B1 | | Whichever pod is |
| D0 (–) | A2 | | plugged into the | D1 (-) | B2 | | plugged into the |
| Ground | A3 | | E5405A probe | D1 (+) | B3 - | > 1 | E5405A probe |
| D2 (+) | A4 → | 2 | _ | Ground | B4 | | _ |
| D2 (–) | A5 | | | D3 (–) | B5 | | _ |
| Ground | A6 | | | D3 (+) | B6 - | > 3 | |
| D4 (+) | A7 -> | 4 | | Ground | B7 | | |
| D4 (-) | A8 | | _ | D5 (–) | B8 | | _ |
| Ground | A9 | | _ | D5 (+) | B9 - | > 5 | _ |
| D6 (+) | A10 -> | 6 | _ | Ground | B10 | | _ |
| D6 (–) | A11 | | _ | D7 (-) | B11 | | _ |
| Ground | A12 | | _ | D7 (+) | B12 - | > 7 | _ |
| NC | A13 | | _ | Ground | B13 | | _ |
| NC | A14 | | _ | Clock- | B14 | | _ |
| Ground | A15 | | _ | Clock+ | B15 - | Clock | _ |
| D8 (+) | A16 → | 8 | _ | Ground | B16 | | _ |
| D8 (–) | A17 | | _ | D9 (-) | B17 | | _ |
| Ground | A18 | | _ | D9 (+) | B18 - | > 9 | _ |
| D10 (+) | A19 -> | 10 | _ | Ground | B19 | | _ |
| D10 (-) | A20 | | _ | D11 (-) | B20 | | _ |
| Ground | A21 | | _ | D11 (+) | B21 - | ▶ 11 | _ |
| D12 (+) | A22 -> | 12 | _ | Ground | B22 | | _ |
| D12 (–) | A23 | | _ | D13 (-) | B23 | | _ |
| Ground | A24 | | _ | D13 (+) | B24 - | ► 13 | _ |
| D14 (+) | A25 → | 14 | _ | Ground | B25 | | _ |
| D14 (-) | A26 | | _ | D15 (-) | B26 | | _ |
| Ground | A27 | | | D15 (+) | B27 - | > 15 | |

probes.

Probing Solutions for 90-pin Logic Analyzers Soft Touch Connectorless Probing (Continued)

Top view E5387A, E5390A Probe dimensions 45.87 mm The following figures show dimensions, 160.79 mm 1.806 in. 6.330 in. - 15.26 footprint, and pinout information you 21.08 mm will need to design your target system 0.830 in. 8.76 mm board for use with the Keysight soft touch 0.345 in. <u>64.48 mm</u> 2.538 in. 34.61 mm 1.363 in. 27.93 mm 1.100 in. Side view E5387A 6.63 mm 0.261 in. 15 7.54 mm 0.297 in. 5.31 mm 0.209 in. Side view E5390A 6.63 mm 0.261 in. 7.54 mm 0.297 in. <u>5.31 mm</u> 0.209 in. Top view E5398A



Figure 6.13. Probe dimensions.

Retention module dimensions

The soft touch probes are attached to the PC board using a retention module which ensures pin-to-pad alignment and holds the probe in place. A board thickness of up to 2.54 mm (0.100 inch) is recommended. Insert the retention module into the board, noting the keying pin, and solder the four alignment pins to the backside of the board.



17-channel retention module dimensions

34-channel retention module dimensions



Figure 6.14. Retention module dimensions.

Probe and retention module dimensions

The following dimensions show the soft touch probe attached to the retention module. The retention module is mounted on the PC board. 34-channel probe and retention module dimensions



17-channel probe and retention module dimensions



Figure 6.15. Probe and retention module dimensions.

Probe footprint dimensions

Use these probe footprint dimensions for the PC board pads and holes for attaching the retention module.

Soft touch



Half-size soft touch



Notes:

- 1. Must maintain a solder mask web between pads when traces are routed between the pads on the same layer. Soldermask may not encroach onto the pads within the pad dimension shown.
 - dermask may not n shown. 5. Retention module dimensions are 34.04 mm x 7.01 mm x 4.98 mm tall relative to the top surface of the PDB. Retention pins extend
- 2. Via in pad not allowed on these pads. Via edges may be tangent to pad edges as long as a solder mask web between vias and pads is maintained.
- 3. Permissible surface finishes on pads are HASL, immersion silver, or gold over nickel.
- 4.32 mm beyond the bottom surface of the RM through the PCB.6. Assume normal artwork tolerances for pad size dimensions.

4. Footprint is compatible with retention module,

Pinout for the E5387A differential soft touch probe

The following graphic and table show the E5387A differential soft touch probe pad numbers and logic analyzer pod inputs.



Logic analyzer

Footprint keep out boundary

Figure 6.17. Pinout.

E5387A differential probe

| E000/A differential probe | | | | | | |
|---------------------------|-------|------------------|-------|---------------|---------|--|
| Negative signa | ls | Positive signals | | | | |
| Signal name | Pad # | Signal name | Pad # | | Channel | Pod |
| D0 (-) | A1 | D0 (+) | B1 | \rightarrow | 0 | Whichever pod is plugged into the E5387A probe |
| D1 (–) | A2 | D1 (+) | B2 | \rightarrow | 1 | |
| Ground | A3 | Ground | B3 | | | |
| D2 (–) | A4 | D2 (+) | B4 | <i>></i> | 2 | |
| D3 (–) | A5 | D3 (+) | B5 | → | 3 | |
| Ground | A6 | Ground | B6 | | | |
| D4 (–) | A7 | D4 (+) | B7 | \rightarrow | 4 | |
| D5 (–) | A8 | D5 (+) | B8 | <i>></i> | 5 | |
| Ground | A9 | Ground | B9 | | | |
| D6 (–) | A10 | D6 (+) | B10 | \rightarrow | 6 | |
| D7 (–) | A11 | D7 (+) | B11 | \rightarrow | 7 | |
| Ground | A12 | Ground | B12 | | | |
| Clock (–) | A13 | Clock (+) | B13 | \rightarrow | Clock | |
| Ground | A14 | Ground | B14 | | | |
| D8 (–) | A15 | D8 (+) | B15 | \rightarrow | 8 | |
| D9 (–) | A16 | D9 (+) | B16 | \rightarrow | 9 | |
| Ground | A17 | Ground | B17 | | | |
| D10 (–) | A18 | D10 (+) | B18 | \rightarrow | 10 | |
| D11 (–) | A19 | D11 (+) | B19 | > | 11 | |
| Ground | A20 | Ground | B20 | | | |
| D12 (–) | A21 | D12 (+) | B21 | \rightarrow | 12 | |
| D13 (–) | A22 | D13 (+) | B22 | > | 13 | |
| Ground | A23 | Ground | B23 | | | |
| D14 (–) | A24 | D14 (+) | B24 | \rightarrow | 14 | |
| D15 (–) | A25 | D15 (+) | B25 | \rightarrow | 15 | |
| Ground | A26 | Ground | B26 | | | |
| N/C | A27 | N/C | B27 | | | |



Figure 6.18. Pinout.

E5398A 17-channel single-

| ended probe | | | Logic analyzer | | | | | |
|-------------|-------|---------------|----------------|------------------|--|--|--|--|
| Signal name | Pad # | | Channel | Pod | | | | |
| D1 | A1 | \rightarrow | 1 | Whichever pod is | | | | |
| D3 | A2 | \rightarrow | 3 | plugged into the | | | | |
| Ground | A3 | | | E5398A probe | | | | |
| D5 | A4 | → | 5 | | | | | |
| D7 | A5 | \rightarrow | 7 | _ | | | | |
| Ground | A6 | | | - | | | | |
| D9 | A7 | → | 9 | | | | | |
| D11 | A8 | → | 11 | _ | | | | |
| Ground | A9 | | | - | | | | |
| D13 | A10 | → | 13 | | | | | |
| D15 | A11 | → | 15 | | | | | |
| Ground | A12 | | | _ | | | | |
| Clock (–) | A13 | ≯ | n/a | | | | | |

| E5398A 17-channel single-ended | | | | | | | | |
|--------------------------------|-------|---------------|----------------|------------------|--|--|--|--|
| probe | | | Logic analyzer | | | | | |
| Signal name | Pad # | | Channel | Pod | | | | |
| DO | B1 | \rightarrow | 0 | Whichever pod is | | | | |
| D2 | B2 | \rightarrow | 2 | plugged into the | | | | |
| Ground | B3 | | | E5398A probe | | | | |
| D4 | Β4 | \rightarrow | 4 | _ | | | | |
| D6 | B5 | \rightarrow | 6 | _ | | | | |
| Ground | B6 | | | _ | | | | |
| D8 | B7 | \rightarrow | 8 | _ | | | | |
| D10 | B8 | \rightarrow | 10 | _ | | | | |
| Ground | B9 | | | | | | | |
| D12 | B10 | \rightarrow | 12 | | | | | |
| D14 | B11 | \rightarrow | 14 | _ | | | | |
| Ground | B12 | | | _ | | | | |
| Clock (+) | B13 | \rightarrow | n/a | | | | | |

Pinout for the E5390A single-ended soft touch probe

The following graphic and table show the E5390A single-ended soft touch probe pad numbers and logic analyzer pod inputs.

| POD 1 | POD 2 | | | | |
|--|---|--|--|--|--|
| D0 D2 G D4 D6 G D8 D10 G D12D14 G G A1 000000000000000000000000000000000000 | Image: Constraint of the constraint | | | | |
| POD 1 | POD 2 | | | | |

Figure 6.19. Pinout.

| E5390A single-ended | d probe | | Logic analy | zer | E5390A single-ended | probe | | Logic analy | zer |
|---------------------|---------|---------------|-------------|-----------------------|---------------------|-------|---------------|-------------|------------------------|
| Signal name | Pad # | | Channel | Pod | Signal name | Pad # | | Channel | Pod |
| D1 | A1 | \rightarrow | 1 | Whichever pod is | Ground | A14 | | | Whichever pod is |
| D3 | A2 | \rightarrow | 3 | connected to "Odd" on | D1 | A15 | → | 1 | connected to "Even" or |
| Ground | A3 | | | the E5390A probe | D3 | A16 | → | 3 | the E5390A probe |
| D5 | A4 | \rightarrow | 5 | - | Ground | A17 | | | - |
| D7 | A5 | \rightarrow | 7 | | D5 | A18 | \rightarrow | 5 | |
| Ground | A6 | | | - | D7 | A19 | → | 7 | - |
| D9 | Α7 | \rightarrow | 9 | - | Ground | A20 | | | - |
| D11 | A8 | \rightarrow | 11 | - | D9 | A21 | \rightarrow | 9 | |
| Ground | A9 | | | - | D11 | A22 | → | 11 | - |
| D13 | A10 | \rightarrow | 13 | - | Ground | A23 | | | - |
| D15 | A11 | \rightarrow | 15 | - | D13 | A24 | \rightarrow | 13 | |
| Ground | A12 | | | - | D15 | A25 | \rightarrow | 15 | |
| Clock (–) | A13 | \rightarrow | Clock | - | Ground | A26 | | | - |
| DO | B1 | \rightarrow | 0 | - | Clock (–) | A27 | \rightarrow | Clock | - |
| D2 | B2 | \rightarrow | 2 | - | Ground | B14 | | | |
| Ground | Β3 | | | | DO | B15 | → | 0 | |
| D4 | Β4 | \rightarrow | 4 | - | D2 | B16 | \rightarrow | 2 | - |
| D6 | B5 | \rightarrow | 6 | - | Ground | B17 | | | |
| Ground | B6 | | | _ | D4 | B18 | \rightarrow | 4 | - |
| D8 | B7 | \rightarrow | 8 | _ | D6 | B19 | \rightarrow | 6 | - |
| D10 | B8 | → | 10 | - | Ground | B20 | | | - |
| Ground | B9 | | | - | D8 | B21 | \rightarrow | 8 | - |
| D12 | B10 | → | 12 | - | D10 | B22 | \rightarrow | 10 | - |
| D14 | B11 | \rightarrow | 14 | - | Ground | B23 | | | - |
| Ground | B12 | | | - | D12 | B24 | \rightarrow | 12 | - |
| Clock (+) | B13 | \rightarrow | Clock | - | D14 | B25 | \rightarrow | 14 | - |
| | | | | | Ground | B26 | | | - |
| | | | | | Clock (+) | B27 | → | Clock | |

Equivalent probe loads

The following probe load models are based on in-circuit measurements made with a Keysight 8753E 6 GHz network analyzer and a Keysight 54750A TDR/TDT using a 50 Ω test fixture. The following schematic accurately models the probe load out to 6 GHz. PC board pads are not included.



Figure 6.20. Equivalent probe load model.

Probing Solutions for 90-pin Logic Analyzers Mictor and Samtec Probing

E5378A 100-pin single-ended probe

The E5378A is a 34-channel single-ended Samtec probe capable of capturing data up to 1.5 Gbits/sec (see Figures 7.3 and 7.5 for probe dimensions and equivalent load). The probe has the following inputs:

- 32 single-ended data inputs, in two groups (pods) of 16.
- Two differential clock inputs. Either or both clock inputs can be acquired as data inputs if not used as a clock.
- Two data threshold reference inputs, one for each pod (group of 16 data inputs).

E5379A 100-pin differential probe

The E5379A is a 17-channel differential Samtec probe capable of capturing data up to 1.5 Gbits/sec (see Figures 7.5 and 7.6 for probe dimensions and equivalent load). The probe has the following inputs:

- 16 differential data inputs.
- One differential clock input.
- The clock input can be acquired as a data input if it is not used as a clock.

Refer to Table 8 on page 61 for part numbers for mating connectors and shrouds.



Figure 6.21. Keysight E5378A probe.

Figure 6.22. Keysight E5379A probe.

Probing Solutions for 90-pin Logic Analyzers Mictor and Samtec Probing (Continued)

E5380A/B 38-pin probe

The E5380A/B is designed to be compatible with the Mictor connector. If you have a target system designed for connection to the E5346A high-density probe adapter, the E5380A/B probe will connect directly to this same Mictor connector. (For information on the E5346A, refer to pages 25 to 26). The maximum state speed when used with the E5380A/B probe is 600 Mbits/second. The minimum input signal amplitude required by the E5380A/B is 300 mV.

The E5380A/B probe combines two 17-channel cables into a single-ended 38-pin Mictor connector.

Refer to Table 8 for connector, shroud, and kit part numbers.



For further information on designing the E5378A, E5379A, or E5380A/B probe connectors into your system, refer to the following documents:

| Keysight Logic Analyzer Probes (E5378A, E5379A, E5380A/B, and E5386A) User's Guide | Mechanical drawings, electrical models, general information on probes for logic analyzers with 90-pin connectors | 16760-97016 http://literature.cdn.keysight.com/litweb/pdf/16760-97016.pdf |
|--|--|--|
| Designing High-Speed Digital Systems | Design recommendations, examples, and | 5988-2989EN |
| for Logic Analyzer Probing | analysis for layout of target systems | http://literature.cdn.keysight.com/litweb/pdf/5988-2989EN.pdf |

Probing Solutions for 90-pin Logic Analyzers Mictor and Samtec Probing (Continued)





Figure 7.2. Dimensions of the 100-Pin Samtec connector used in the 16760-68702 and 16760-68703 connector kits.





Figure 7.4. E5379A 100-pin differential probe dimensions.





Figure 7.5. E5378A and E5379A input equivalent load, including 100-pin connector.



Figure 7.7. E5380A/B input equivalent load, including 38-pin connector.



Probing Solutions for 90-pin Logic Analyzers High-Speed Timing Probing

E5386A half-channel adapter

When the Keysight high-speed timing analyzers are operating at their maximum speed, only the even numbered channels are used. To reduce the number of probes and connectors required, the E5386A adapter maps the even channels to all of the pins of an E5378A and E5379A Samtec probes, E5387A and E5390A Soft Touch Connectorless probes, and Soft Touch Pro Series connectorless probes E5404A, E5405A, and E5406A. The following diagrams show how the E5386A is connected.



Figure 8.1. E5386A half-channel probe adapter.



Figure 8.2. E5386A with E5378A, E5390A, or E5406A single-ended probe. Figure 8.3. E5386A with E

Figure 8.3. E5386A with E5379A, E5387A, or E5405A differential probe.

For further information on the application of the E5386A Half-channel adaptor refer to Keysight E5400-Pro Series Soft Touch Connectorless Probes User's Guide, publication number E5404-97006.pdf

General-Purpose Probing Flying Lead Probing Accessories

Wedge adapters

The Keysight Wedge technology provides very reliable probing of a few channels on 0.5 mm and 0.65 mm pitch QFPs. No clear area is required around the device. Each Wedge of the probe slides between the legs of the QFP. The side of each Wedge probe contacts the package legs. An insulation core electrically isolates the sides of each Wedge (see Figures 9.1 and 9.2). Various 3-signal, 8-signal, and 16-signal probes are available (see Table 9).

Figure 9.1. Three-signal Wedge electrical connection

Table 9. Wedge probe adapter

| IC leg spacing | Number of signals | Number of wedges in pack | Model number |
|----------------|-------------------|--------------------------|--------------|
| 0.5 mm | 3 | 1 | E2613A |
| 0.5 mm | 3 | 2 | E2613B |
| 0.5 mm | 8 | 1 | E2614A |
| 0.5 mm | 16 | 1 | E2643A |
| 0.65 mm | 3 | 1 | E2615A |
| 0.65 mm | 3 | 2 | E2615B |
| 0.65 mm | 8 | 1 | E2616A |
| 0.65 mm | 16 | 1 | E2644A |



Top view of 16 signal pins

Bottom view of 16 ground pins (connected to common ground plane)



Figure 9.2. Eight-signal and 16-signal Wedge (16-signal Wedge has a common ground plane).

Miscellaneous probing accessories

The ferrite core assembly can be added to the probe cable to suppress EMI and RFI noise that can corrupt the measurement.



Figure 9.3. Ferrite core assembly, 16555-60001.

40-pin and 90-pin Logic Analyzers Probe Cables

Signal line loading

Any probed signal line must be able to supply a minimum of 600 mV (unless noted otherwise – see probe of interest) to the probe tip while the probe is connected to the system. The maximum input voltage of each probe is \pm 40 volts peak (unless noted otherwise – see probe of interest).

Probe cables

The probe cable (see Figure 10.1 and Table 10) contains 16 signal lines and two clk lines, two +5 volt power lines, and ground lines for each of the signal/clock and power lines. All of these lines are contained in a 4.5-foot cable. The probe cable is included with the logic analyzer. The cable grounds are chassis (earth) grounds, not "floating" grounds. The two +5 volt power lines can be used to power active probing systems. Consult the specifications for the individual logic analyzers or logic analyzer cards for the maximum allowable current through each +5 volt power supply.

Caution: These +5 volt power lines MUST NOT be connected to the target's power supply.

Caution: Be careful when using straight wire probe leads, one common ground, or RC networks located far from the target. These circumstances increase the impact of analog effects such as crosstalk and EMT susceptibility, which contribute to measurement errors.



Figure 10.1. Typical logic analyzer probe cable.

40-pin and 90-pin Logic Analyzers Probe Cables (Continued)

Table 10. Probe cables supplied with Keysight logic analyzers

| 40-pin cable part number | | | | | | 90-pin cable part number | | | |
|--------------------------|-------------|-------------|-------------|-------------|-------------|--------------------------|-------------|-------------|--------|
| Logic analyzer | 01550-61607 | 16550-61601 | 01660-61605 | 16555-61606 | 16710-61603 | 16715-61601 | 16760-61605 | 16962-61601 | U4201A |
| U4154A | | | | | | | | | • |
| 16962A | | | | | | | | • | |
| 16951B | | | | | | | • | | |
| 16950A/B | | | | | | | • | | |
| 16911A | | | | | | • | | | |
| 16910A | | | | | | • | | | |
| 16800 Series | | | | | | • | | | |
| 16760A | | | | | | | • | | |
| 16753/54/ | | | | | | | • | | |
| 55/56A | | | | | | | | | |
| 16752A/B | | | | | | • | | | |
| 16751A/B | | | | | | • | | | |
| 16750A/B | | | | | | • | | | |
| 16740 Series | | | | | | • | | | |
| 16719A | | | | | | • | | | |
| 16718A | | | | | | • | | | |
| 16717A | | | | | | • | | | |
| 16716A | | | | | | • | | | |
| 16715A | | | | | | • | | | |
| 16712A | | | | | • | | | | |
| 16711A | | | | | • | | | | |
| 16710A | | | | | • | | | | |
| 16557D | | | | | • | | | | |
| 16556A/D | | | | • | | | | | |
| 16555A/D | | | | • | | | | | |
| 16554A | | | | • | | | | | |
| 16550A | | | • | | | | | | |
| 1690 Series | | | | | | • | | | |
| 1680 Series | | | | | | • | | | |
| 1670 Series | | | | | • | | | | |
| 1660 Series | | • | | | | | | | |
| 1650 Series | • | | | | | | | | |

Related Information

Keysight logic analysis third-party partners: For a complete list of partners, see document 5966-4365EUS **Processor and Bus Support for Keysight Technologies Logic Analyzers**

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http://www.mmm.com/interconnects

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Keysight Technologies Test and Measurement Logic Analyzers web site: http://www.keysight.com/find/logic

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This document does not cover the following topics:

- Pattern generator probing and accessories See:
 - Keysight 16800 Series Portable Logic Analyzers Data Sheet, publication number 5989-5063EN
 - Keysight Technologies Measurement Modules for the 16900 Series Data Sheet, publication number 5989-0422EN
- Analysis probes for processors and buses See:
 - Processor and Bus Support for Keysight Technologies Logic Analyzers Configuration Guide, publication number 5966-4365E

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